ABSTRACT OF THE DISCLOSURE

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A dual stage current limiting surge protector system for protecting telecommunications equipment from power and surge transients is provided. The surge protector system includes a voltage suppressor having first and second ends operatively coupled between input tip and ring terminal pins. The first and second ends of the voltage suppressor are also operatively coupled between output tip and ring terminal pins. First and second current limiting devices are interconnected between the input tip and ring terminal pins and the respective first and second ends of the voltage suppressor. Third and fourth current limiting devices are interconnected between the output tip and ring terminal pins and the respective first and second ends of the voltage suppressor.